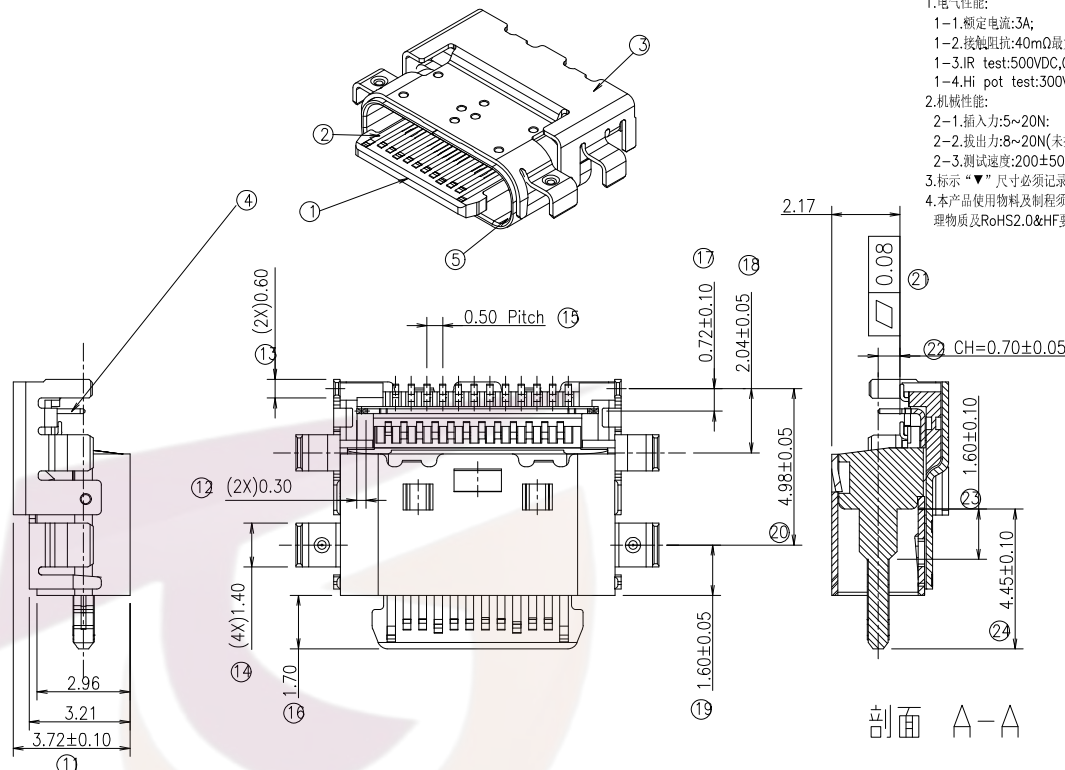
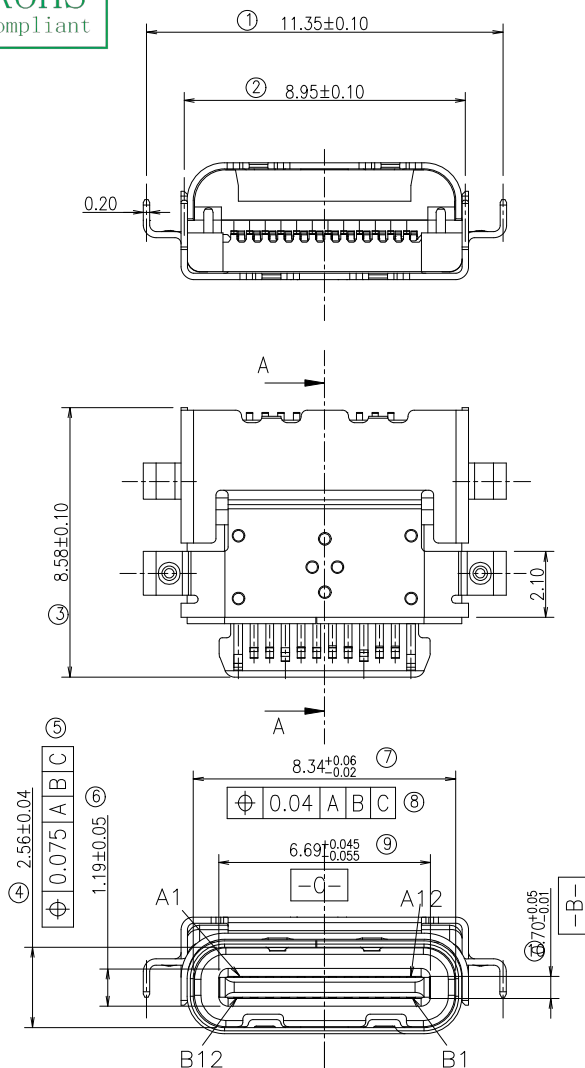


RoHS
Compliant

说明:

- 1.电气性能:
 - 1-1.额定电流:3A;
 - 1-2.接触电阻:40mΩ最大;
 - 1-3.IR test:500VDC,0.1S,50MΩ;
 - 1-4.Hi pot test:300VAC,0.1S,0.5mA;
 - 2.机械性能:
 - 2-1.插入力:5~20N;
 - 2-2.拔出力:8~20N(未插过),6~20N(10000次耐久测试后);
 - 2-3.测试速度:200±50次/小时,测试10000次;
- 3.标示“▼”尺寸必须记录实际检验数据,标示“⊗”的尺寸重点确认;
4.本产品使用物料及制程须符合“QW-Q-043”文件对应环境管理物质及RoHS2.0&HF要求;



5	INTERNAL SHELL	SUS304-1/2H	SOLDERABLE NI 50u" Min. OVER ALL	1
4	MID-PLATE	SUS301-H	SOLDER AREA:G/F;NI 50u"Min. OVER ALL.	1
3	SHELL	SUS304-1/2H	SOLDERABLE NI 50u" Min. OVER ALL	1
2	CONTACT	C7025-TM06	CONTACT AREA:Au 3u" Min.;SOLDER AREA:G/F;NI 50u"~150u". OVER ALL.	24
1	HOUSING	PA9T	BLACK,UL 94V-0	1
ITEM	DEFINITION	MATERIAL	FINISH	Q'TY

Guangdong Tengjun Electronic Technology Co., Ltd

Website:

<https://www.tengjunconn.com>

TEL:

0769-23323869



PART NAME

Type C 沉板CH=0.7 (Mi8 SE)

PART NO.

TJ2824FSW100858B0

TOLERANCE UNLESS
OTHERWISE SPECIFIED:

X ±0.2
X.X ±0.15
X.XX ±0.10
X.XXX ±0.050

DRAWING:

Li

DATE:

2023.09.18

SCALE

SHEET

REV: A0

CHECK:

Chen

DATE:

2023.09.18

1:1

1/2

SIZE: A4

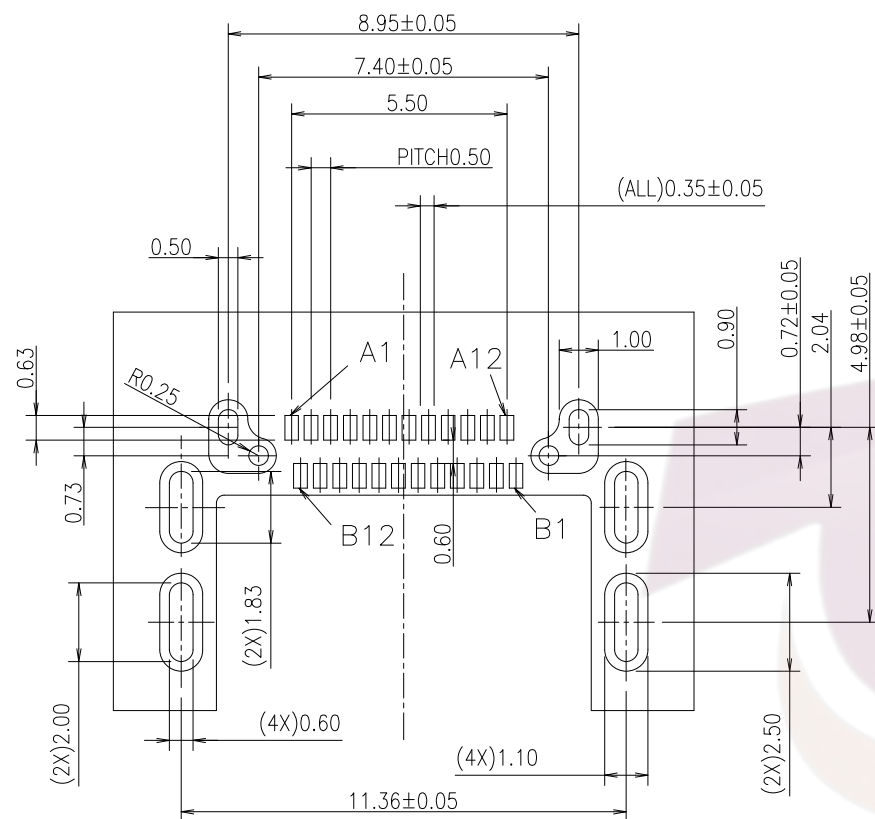
APPROVAL:

Yuan

DATE:

2023.09.18

☐



Pin Assignments

Pin	Signal Name	Description	Mating Sequence	Pin	Signal Name	Description	Mating Sequence
A1	GND	Ground return	First	B12	GND	Ground return	First
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	Second	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair	Second
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	Second	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair	Second
A4	VBUS	Bus Power	First	B9	VBUS	Bus Power	First
A5	CC1	Configuration Channel	Second	B8	SBU2	Sideband Use	Second
A6	Dp1	Positive half of the USB2.0 differential pair-Position 1	Second	B7	Dn2	Negative half of the USB2.0 differential pair-Position 2	Second
A7	Dn1	Negative half of the USB2.0 differential pair-Position 1	Second	B6	Dp2	Positive half of the USB2.0 differential pair-Position 2	Second
A8	SBU1	Sideband Use	Second	B5	CC2	Configuration Channel	Second
A9	VBUS	Bus Power	First	B4	VBUS	Bus Power	First
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	Second	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair	Second
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	Second	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair	Second
A12	GND	Ground return	First	B1	GND	Ground return	First

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Li

DATE:

2023.09.18

SCALE

SHEET

REV: A0

CHECK:

Chen

DATE:

2023.09.18

1:1

2/2

SIZE: A4

APPROVAL:

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